



may be transmitted on channels includes channel names themselves; this, together with the ability to dynamically create new channel names, gives the language its descriptive power.

Within the setting of the  $\pi$ -calculus we wish to investigate the use of types to enforce security policies. To facilitate the discussion we extend the syntax with a new construct to represent a process running at a given security clearance,  $\sigma\llbracket$

appropriate.

This policy does not rule out the possibility of information leaking indirectly from high security to low security principals. Suppose  $h$  is a high channel and  $hl$  is a channel with high-level write access and low-level read access in:

$$\text{top} \llbracket h?(x) \text{ if } x = 0 \text{ then } hl!\langle 0 \rangle \text{ else } hl!\langle 1 \rangle \rrbracket \mid \text{bot} \llbracket hl?(z) \rrbracket$$

This system can be well-typed although there is some implicit information flow from the high security agent to the low security one; the value received on the high level channel  $h$  can be determined by the low level process.

It is difficult to formalize exactly what is meant by *implicit information flow* and in the literature various authors have instead relied on *non-interference*, [14, 25, 11, 26], a concept more amenable to formalization, which ensures, at least informally, the absence of implicit information flow.

To obtain such results for the  $\pi$ -calculus we need, as the above example shows, a stricter security policy, which we refer to as the *I-security policy*. This allows a high level principal to read from low level resources but not to write to them. Using the terminology of [2, 7]:

- *write up*: a process at level  $\sigma$  may only write to channels at level  $\sigma$  or above
- *read down*: a process at level  $\sigma$  may only read from channels at level  $\sigma$  or below.

In fact the type inference system remains the same and we only need constrain the notion of type. In this restricted type system well-typing,  $\Gamma \Vdash P$ , ensures a form of *non-interfer*



FIGURE 2 Labelled Transition Semantics

$$\begin{array}{c}
 \text{(L-OUT)} \qquad \qquad \text{(L-IN)} \\
 \hline
 \frac{}{a!\langle v \rangle \xrightarrow{a!v} \mathbf{0}} \qquad \frac{}{a?(X) P \xrightarrow{(\tilde{c}:\tilde{C})a?v} P\{v/X\}} \quad \tilde{c} \notin \text{fn}(P) \\
 \\
 \text{(L-OPEN)} \\
 \frac{P \xrightarrow{(\tilde{c}:\tilde{C})a!v} P'}{(\text{n w } b:\mathbf{B}) P \xrightarrow{(b:\mathbf{B})(\tilde{c}:\tilde{C})a!v} P'} \quad \begin{array}{l} b \neq a \\ b \in \text{fn}(v) \end{array} \\
 \\
 \text{(L-COM)} \\
 \frac{P \xrightarrow{\alpha} P', \quad \bar{\alpha} \xrightarrow{\quad} P'}{P \mid \quad \xrightarrow{\tau} (\text{n w } \mathcal{E}(\alpha)) (P' \mid P')} \\
 \\
 \text{(L-EQ)} \\
 \\
 \frac{}{\text{if } u = u \text{ th n } P \text{ ls } \xrightarrow{\tau} P} \qquad \frac{}{\text{if } u = w \text{ th n } P \text{ ls } \xrightarrow{\tau} P} \quad u \neq w \\
 \\
 \text{(L-CTXT)} \\
 \frac{P \xrightarrow{\mu} P'}{*P \xrightarrow{\mu} *P \mid P'} \qquad \frac{P \xrightarrow{\mu} P'}{P \mid \quad \xrightarrow{\mu} P' \mid \quad} \quad \text{bn}(\mu) \notin \text{fn}(Q) \\
 \sigma[P] \xrightarrow{\mu} \sigma[P'] \qquad \quad \quad \mid P \xrightarrow{\mu} \quad \mid P' \\
 \\
 \frac{P \xrightarrow{\mu} P'}{(\text{n w } a:\mathbf{A}) P \xrightarrow{\mu} (\text{n w } a:\mathbf{A}) P'} \quad a \notin \text{n}(\mu)
 \end{array}$$

a set of *basic values*  $BV_\sigma$ ; we use  $bv$  to range over base values. We require that all syntactic sets be disjoint.

The input construct  $u?(X:\mathbf{A}) P$  binds all variables in the pattern  $X$  while the construct  $(\text{n w } a:\mathbf{A}) P$  binds names and associated with these. We have the usual notions of free and bound names and variables,  $\alpha$ -equivalence and substitution. We identify terms up to  $\alpha$ -equivalence. Let  $\text{fn}(P)$  and  $\text{fv}(P)$  denote the set of free names and variables, respectively, of the term  $P$ . We use  $P\{v/X\}$  to denote the substitution of the identifiers occurring in the value  $v$  for the variables occurring in the pattern  $X$ . For  $P\{v/X\}$  to be well-defined  $X$  and  $v$  must have the same structure; to avoid unnecessary complications we assume that a variable can occur at most once in a pattern. The binding constructs have types associated with them; these will be explained in Section 3 but are ignored for the moment. In general these types (and the various security annotations) will be omitted from terms unless they are relevant to the discussion at hand.

The behaviour of a process is determined by the interactions in which it can engage. To define these, we give a labelled transition semantics (LTS) for the language. The set *Act* of *labels*, or *actions*, is defined as

this end, *Pre-capabilities* and *pre-types* are defined as follows:

$cap ::=$	<i>Pre-Capability</i>
$w_\sigma \langle A \rangle$	$\sigma$ -level process can write values with type A
$r_\sigma \langle A \rangle$	$\sigma$ -level process can read values with type A
$A ::=$	<i>Pre-Type</i>
$\mathbf{B}_\sigma$	Base type
$\{cap_1, \dots, cap_k\}$	Resource type ( $k \geq 0$ )
$(A_1, \dots, A_k)$	Tuple type ( $k \geq 0$ )

We will tend to abbreviate a singleton set of capabilities,  $\{cap\}$ ,

FIGURE 3 Runtime Errors

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(E-RD)	$\rho \llbracket a?(X) P \rrbracket \vdash_{\Sigma}^{\rightarrow} err$	if $\sigma \preceq \rho$ implies for all $A$ , $r_{\sigma} \langle A \rangle \notin \Sigma(a)$
(E-WR <sub>1</sub> )	$\rho \llbracket a! \langle v \rangle \rrbracket \vdash_{\Sigma}^{\rightarrow} err$	if $\sigma \preceq \rho$ implies for all $A$ , $w_{\sigma} \langle A \rangle \notin \Sigma(a)$
(E-WR <sub>2</sub> )	$\rho \llbracket a! \langle v \rangle \rrbracket \vdash_{\Sigma}^{\rightarrow} err$	if $bv \in v$ , $bv \in \mathbf{B}_{\sigma}$ and $\sigma \not\preceq \rho$
(E-STR)	$\frac{\frac{P \vdash_{\Sigma}^{\rightarrow} err}{P \mid \vdash_{\Sigma}^{\rightarrow} err} \quad \frac{P \vdash_{\Sigma}^{\rightarrow} err}{\rho \llbracket P \rrbracket \vdash_{\Sigma}^{\rightarrow} err} \quad \frac{P \equiv \quad, P \vdash_{\Sigma}^{\rightarrow} err}{\vdash_{\Sigma}^{\rightarrow} err}}{\frac{P \vdash_{\Sigma, a:A}^{\rightarrow} err}{(n \ w \ n : A) P \vdash_{\Sigma}^{\rightarrow} err}}$	

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$c! \langle lh \rangle$  although intuitively it involves a security leak; a low security agent can read from  $c$  a channel which has at least some capability which should only be accessible to high security principals. However it is straightforward to place it in a context in which a security leak occurs:  $c! \langle lh \rangle \mid \text{bot} \llbracket c?(x) x! \langle v \rangle \rrbracket$ . Thus our typing system will also be required to rule out such processes.  $\square$

### 3 Resource Control

Our typing system will apply only to certain security policies, those in which the pre-types are in some sense *consistent*. Consistency is imposed using a system of kinds: the



For each  $\rho$ , let  $RType_\rho$  be the least set that satisfies:

$$\begin{array}{c}
 \text{(RT-WR)} \\
 \frac{A \in RType_\sigma}{\{w_\sigma\langle A \rangle\} \in RType_\rho} \quad \sigma \preceq \rho \\
 \\
 \text{(RT-RD)} \\
 \frac{A \in RType_\sigma}{\{r_\sigma\langle A \rangle\} \in RType_\rho} \quad \sigma \preceq \rho \\
 \\
 \text{(RT-WRRD)} \\
 \frac{A \in RType_\sigma \quad A' \in RType_{\sigma'}}{\{w_\sigma\langle A \rangle, r_{\sigma'}\langle A' \rangle\} \in RType_\rho} \quad \begin{array}{l} \sigma \succcurlyeq \rho \\ \sigma' \succcurlyeq \rho \\ A \triangleleft A' \end{array} \\
 \\
 \text{(RT-TUP)} \\
 \frac{\mathbf{B}_\sigma \in RType_\rho \quad A_i \in RType_\rho \quad (\forall i)}{(A_1, \dots, A_k) \in RType_\rho} \quad \sigma \preceq \rho \\
 \\
 \text{(RT-BASE)} \\
 \frac{}{\mathbf{B}_\sigma \in RType_\rho} \quad \sigma \preceq \rho
 \end{array}$$

Let  $RType$  be the union of the kinds  $RType_\rho$  over all  $\rho$ . □

Note that if  $\sigma \preceq \rho$  then  $RType_\sigma \subseteq RType_\rho$ . Intuitively, low level values are accessible to high level processes. However the converse is not true. For example  $w_{\text{top}}\langle \rangle \in RType_{\text{top}}$  but  $w_{\text{top}}\langle \rangle$  is not in  $RType_{\text{bot}}$ . Note also that there is no relation between subtyping and accessibility at a given security level. For example:

$$\begin{array}{l}
 w_{\text{bot}}\langle \rangle \in RType_{\text{bot}} \text{ and } \{w_{\text{bot}}\langle \rangle, r_{\text{top}}\langle \rangle\} \triangleleft: r_{\text{bot}}\langle \rangle \text{ but } \{w_{\text{bot}}\langle \rangle, w_{\text{top}}\langle \rangle\} \notin RType_{\text{bot}} \\
 r_{\text{bot}}\langle \rangle \in RType_{\text{bot}} \text{ and } r_{\text{bot}}\langle \rangle \triangleleft: r_{\text{top}}\langle \rangle \text{ but } r_{\text{top}}\langle \rangle \notin RType_{\text{bot}}
 \end{array}$$

The compatibility requirement between read and write capabilities in a type (RT-WRRD), in addition to the typing implications discussed in [23], also has security implications. For example suppose  $r_{\text{bot}}\langle \mathbf{B}_\sigma \rangle$  and  $w_{\text{top}}\langle \mathbf{B} \rangle$  are capabilities in a valid channel type. Then a priori a high level process can write to the channel while a low level process may read from it. However the only possibility for  $\sigma$  is **bot**, that is only low level values may be read. Moreover the requirement  $\mathbf{B} \triangleleft: \mathbf{B}_\sigma$  implies that  $\mathbf{B}$  must also be  $\mathbf{B}_{\text{bot}}$ . So although high level processes may write to the channel they may only write low level values.

*Remark.* Most of the restrictions imposed on types are essential to achieving Subject Reduction, but a few are not. First, Subject Reduction still holds if we weaken (U-WR) to:  $w_\sigma\langle A \rangle \triangleleft: w_\rho\langle B \rangle$  if  $B \triangleleft: A$  and  $\sigma \preceq \rho$ . Were we to adopt this rule, it would be true that every process typable at level  $\sigma$  would also be typable at level  $\rho$ , for  $\sigma \preceq \rho$ . Given our definition, this is not true. Nonetheless, every process typable at  $\sigma$  can be trivially rewritten so that it is typable at  $\rho$  given our definition (one must simply surround output actions with explicit security restrictions). We have

FIGURE 4 Typing Rules

$\frac{\text{(T-ID)} \quad \Gamma(u) \prec: A}{\Gamma \vdash u : A}$	$\frac{\text{(T-BASE)} \quad bv \in \mathbf{B}_\sigma}{\Gamma \vdash bv : \mathbf{B}_\sigma}$	$\frac{\text{(T-TUP)} \quad \Gamma \vdash v_i : A_i \quad (\forall i)}{\Gamma \vdash (v_1, \dots, v_k) : (A_1, \dots, A_k)}$
$\frac{\text{(T-IN)} \quad \Gamma, X : A \Vdash^\sigma P \quad \Gamma \vdash u : r_\sigma \langle A \rangle}{\Gamma \Vdash^\sigma u?(X : A) P}$	$\frac{\text{(T-OUT)} \quad \Gamma \vdash u : w_\sigma \langle A \rangle \quad \Gamma \vdash v : A}{\Gamma \Vdash^\sigma u! \langle v \rangle}$	$\frac{\text{(T-EQ)} \quad \Gamma \vdash u : A, v : B \quad \Gamma \Vdash^\sigma \Gamma \sqcap \{u : B, v : A\} \Vdash^\sigma P}{\Gamma \Vdash^\sigma \text{if } u = v \text{ th n } P \text{ ls}}$
$\frac{\text{(T-SR)} \quad \Gamma \Vdash^{\sigma \sqcap \rho} P}{\Gamma \Vdash^\sigma \rho \llbracket P \rrbracket}$	$\frac{\text{(T-NEW)} \quad \Gamma, a : A \Vdash^\sigma P}{\Gamma \Vdash^\sigma (\text{n w a} : A) P}$	$\frac{\text{(T-STR)} \quad \Gamma \Vdash^\sigma P,}{\Gamma \Vdash^\sigma P \mid \_, *P, \mathbf{0}}$

adopted the stronger rule because it is necessary in the next section and results in no substantive loss of expressivity.

Second, we have limited types to contain at most one read and one write capability. We have done so to simplify the proofs, particularly in the next section. This clearly results in a loss of expressiveness. We have yet to find, however, a compelling example that requires a resource to have more than one read or one write capability. It is usually sensible to simply take the meet.  $\square$

**PROPOSITION 3.2.** *For every  $\rho$ ,  $RType_\rho$  is a preorder with respect to  $\prec$ , with both a partial meet operation  $\sqcap$  and a partial join  $\sqcup$ .*

*Proof.* Straightforward adaptation of Proposition 6.2 of [23]. The partial operations  $\sqcap$  and  $\sqcup$  are first defined by structural induction on types. Typical clauses are

$$\begin{aligned} r_\sigma \langle A \rangle \sqcap r_{\sigma'} \langle A' \rangle &= r_{\sigma \sqcap \sigma'} \langle A \sqcap A' \rangle \\ w_\sigma \langle A \rangle \sqcap w_{\sigma'} \langle A' \rangle &= w_\sigma \langle A \sqcup A' \rangle \\ r_\sigma \langle A \rangle \sqcup r_{\sigma'} \langle A' \rangle &= r_{\sigma \sqcup \sigma'} \langle A \sqcup A' \rangle \\ w_\sigma \langle A \rangle \sqcup w_{\sigma'} \langle A' \rangle &= w_\sigma \langle A \sqcap A' \rangle \end{aligned}$$

One can then show, by induction on the definitions, that:

$$\begin{aligned} A \in RType_\rho \text{ and } A \in RType_{\rho'} \text{ implies } A \sqcap B \in RType_{\rho \sqcap \rho'} \text{ and} \\ A \sqcup B \in RType_{\rho \sqcup \rho'}. \end{aligned}$$

Finally it is straightforward to show that  $\sqcap$  and  $\sqcup$ , defined in this manner, are indeed partial meet and partial join operators.  $\square$

We now discuss the typing system, which is defined using restricted security policies, called type environments. A *type environment* is a finite mapping from identifiers (names and variables) to types. We adopt some standard notation. For example, let  $\Gamma, u : A$  denote the obvious extension of  $\Gamma$ ;  $\Gamma, u : A$  is only defined if  $u$  is not in the domain of  $\Gamma$ . The subtyping relation  $<$ : together with the partial operators  $\sqcap$  and  $\sqcup$  may also be extended to environments. For example  $\Gamma < \Delta$  if for all  $u$  in the domain of  $\Delta$ ,  $\Gamma(u) < \Delta(u)$ . The partial meet enables us to define more subtle extensions. For example  $\Gamma \sqcap \{u : A\}$  may be defined even if  $u$  is already in the domain of  $\Gamma$ . It is well defined when  $\Gamma(u) \sqcap A$  exists, in which case it maps  $u$  to this type. We will normally abbreviate the simple environment  $\{u : A\}$  to  $u : A$  and moreover use  $v : A$  to denote its obvious generalisation to values; this is only well-defined when the value  $v$  has the same structure as the type  $A$ .

The typing system is given in Figure 4 where the judgements are of the form  $\Gamma \Vdash^\sigma P$ . If  $\Gamma \Vdash^\sigma P$  we say that  $P$  is a  $\sigma$ -level process. Also, let  $\Gamma \vdash P$  abbreviate  $\Gamma \Vdash^{\text{top}} P$ .

Intuitively  $\Gamma \Vdash^\sigma P$  indicates that the process  $P$  will not cause any security errors if executed with security clearance  $\sigma$ . The rules are very similar to those used in papers such as [23, 21] for the standard IO typing of the  $\pi$ -calculus. Indeed the only significant use of the security levels is in the (T-IN) and (T-OUT) rules, where the channels are required to have a specific security level. This is inferred using auxiliary value judgements, of the form  $\Gamma \vdash v : A$ . It is interesting to note that security levels play no direct role in their derivation. One might expect that the judgements for values would need to ensure that a value written to a channel be accessible at the appropriate security level. This job, however, is already handled by our definition of types. For example, in order for  $w_\sigma \langle A \rangle$  to be a type,  $A$  must be a type accessible to  $\sigma$ .

The typing system enjoys many expected properties, the proof of which we leave to the reader.

### PROPOSITION 3.3.

- (SPECIALIZATION)  $\Gamma \vdash v : A$  and  $A < B$  then  $\Gamma \vdash v : B$
- (WEAKENING)  $\Gamma \Vdash^\sigma P$  and  $\Delta < \Gamma$  then  $\Delta \Vdash^\sigma P$
- (RESTRICTION)  $\Gamma, u : A \Vdash^\sigma P$  and  $u \notin \text{fv}(P) \cup \text{fn}(P)$  implies  $\Gamma \Vdash^\sigma P$ .  $\square$

The main technical tool required for Subject Reduction is, as usual, a substitution result.

LEMMA 3.4 (SUBSTITUTION). *If  $\Gamma \vdash v : A$  then*

- $\Gamma \vdash$

(T-STR), followed by (T-NEW), gives the required  $\Gamma \Vdash^{\sigma} (n \text{ w } \mathcal{E}(\alpha)) (P' \mid \cdot)$ .  $\square$

We can now prove the first main result:

**THEOREM 3.6 (TYPE SAFETY).** *If  $\Gamma \vdash P$  then for every closed context  $C[\cdot]$  such that  $\Gamma \vdash C[P]$  and every  $\sigma$  such that  $C[P] \xrightarrow{\tau}^* \text{err}$  we have  $\Gamma \not\vdash^{\sigma} \text{err}$*

*Proof.* By Subject Reduction we know that  $\Gamma \Vdash^{\text{top}}$  and therefore it is sufficient to prove that  $\Gamma \Vdash^{\text{top}}$  implies  $\Gamma \not\vdash^{\sigma} \text{err}$ . In fact we prove the contrapositive,  $\Gamma \vdash^{\sigma} \text{err}$  implies  $\Gamma \not\vdash^{\text{top}}$  by induction on the definition of  $\Gamma \vdash^{\sigma} \text{err}$ .

This is a straightforward inductive proof on the derivation of  $\Gamma \vdash^{\sigma} \text{err}$ . For example consider the case (E-RD). Suppose that  $\rho[a?(X) P] \vdash^{\sigma} \text{err}$  because  $\sigma \preceq \rho$  implies for all  $A$ ,  $r_{\sigma}\langle A \rangle \notin \Sigma(a)$ . By supposition, we have that  $\Gamma(a)$  either has no read capability or it has a read capability at level  $\delta$ , where  $\delta \not\preceq \rho$ . In either case, the judgement  $\Gamma \Vdash^{\rho} a?(X) P$  cannot be derived, and therefore  $\Gamma \not\vdash^{\text{top}} \rho[a?(X) P]$  is also underivable.  $\square$

We end this section with a brief discussion on the use of the syntax  $\sigma[P]$  in our language. We have primarily introduced it in order to discuss typing issues. Having defined our typing system we may now view  $\sigma[P]$  simply as notation for the fact that, relative to the current typing environment  $\Gamma$ , the process  $P$  is well-typed at level  $\sigma$ , i.e.  $\Gamma \Vdash^{\sigma} P$ . Technically we can view  $\sigma[P]$  to be *structurally equivalent* to  $P$ , assuming we are working in an environment  $\Gamma$  such that  $\Gamma \Vdash^{\sigma} P$ . This will be formalised in Section 5.

## 4 Information Flow

We have shown in the previous sections that, in well-typed systems, processes running at a given security level can only access resources appropriate to that level. However, as pointed out in the Introduction this does not rule out (implicit) information flow between levels. Consider the following system

$$\text{top} \llbracket h?(x) \text{ if } x = 0 \text{ then } h!\langle 0 \rangle \text{ else } h!\langle 1 \rangle \rrbracket \mid \text{bot} \llbracket h!(z) \rrbracket \quad (\star)$$

executing in an environment in which  $h$  is a **top**-level read/write channel and  $h!$  is a **top**-level write and **bot**-level read channel. This system can be well-typed, using *R-types*, so the processes only access resources appropriate to their security level. Nevertheless there is some implicit flow of



$\text{top} \llbracket h?(x) \text{ if } x = 0 \text{ then bot} \rrbracket$

Here  $\approx^\sigma$  is some form of behavioural equivalence that is sensitive only to behaviour of processes that are  $\sigma$ -level or lower. It turns out that such a result is very dependent on the exact formulation used, as the following example illustrates.

Let  $A$  denote the type  $\{\mathbf{w}_{\text{bot}}\langle \rangle, \mathbf{r}_{\text{bot}}\langle \rangle\}$  and  $B$  denote  $\{\mathbf{r}_{\text{bot}}\langle \rangle\}$ . Further, let  $\Gamma$  map  $a$  and  $b$  to  $A$  and  $B$ , respectively, and  $n$  to the type  $\{\mathbf{w}_{\text{bot}}\langle A \rangle, \mathbf{r}_{\text{bot}}\langle A \rangle\}$ . Now consider the terms  $P$  and  $H$  defined by

$$P \Leftarrow \mathbf{bot}\llbracket n!\langle a \rangle \mid n?(x:A) \ x!\langle \rangle \rrbracket \quad H \Leftarrow \mathbf{top}\llbracket n?(x:B) \ b?(y) \mathbf{0} \rrbracket$$

It is very easy to check that  $\Gamma \Vdash P, H$  and that  $H$  is  $\mathbf{bot}$ -free. Note that in the term  $P \mid H$  there is contention between the low and high-level processes for who will receive a value on the channel  $n$ . This means that if we were to base the semantic relation  $\approx$  on any of *strong bisimulation equivalence*, *weak bisimulation equivalence*, [18], or *must testing*, [20], we would have

$$P \mid \mathbf{0} \not\approx^\sigma P \mid H$$

The essential reason is that the consumption of writes can be detected; the reduction

$$P \mid H \xrightarrow{\tau} \mathbf{bot}\llbracket n?(x:A) \ x!\langle \rangle \rrbracket \mid \mathbf{top}\llbracket b?(y) \cdot \mathbf{0} \rrbracket$$

cannot be matched by  $P \mid \mathbf{0}$ . Using the terminology of [20],  $P \mid \mathbf{0}$  *guarantees* the test  $\mathbf{bot}\llbracket a?(x) \ \omega!\langle \rangle \rrbracket$  whereas  $P \mid H$  does not.

Even obtaining results with respect to *may testing*, defined in Section 5, is delicate. If we allowed *synchronous* tests then we would also have:

$$P \mid \mathbf{0} \not\approx^\sigma P \mid H$$

Let  $T$  be the test  $\mathbf{bot}\llbracket b!\langle \rangle \ \omega!\langle \rangle \rrbracket$ . Then  $P \mid H \mid T$  may eventually produce an output on  $\omega$  whereas  $P \mid \mathbf{0} \mid T$  cannot. However, since our language is asynchronous, such tests are not allowed.

In the following section, we prove a non-interference result using may testing on processes typable using *I-types*.

## 5 Noninterference up to May Testing

May equivalence is defined in terms of tests. A *test* is a process with an occurrence of a new reserved resource name  $\omega$ . We use  $T$  to range over tests, with the typing rule  $\Gamma \Vdash \omega!\langle \rangle$  for all  $\Gamma$ . When placed in parallel with a process  $P$ , a test may interact with  $P$ , producing an output on  $\omega$  if some desired behaviour of  $P$  has been observed.

**DEFINITION 5.1.** We write  $T \Downarrow$  if  $T \xrightarrow{\tau}^* T'$ , where  $T'$  has the form  $(n \ \mathbf{w} \ \tilde{c}) (\omega!\langle \rangle \mid T'')$  for some  $T''$  and  $\tilde{c}$ .  $\square$



## *Information*

FIGURE 5 Context LTS

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$\frac{\text{(C-RED)} \quad P \xrightarrow{\tau} P'}{\Delta \triangleright P \xrightarrow{\tau}_\sigma \Delta \triangleright P'}$	$\frac{\text{(C-OUT)} \quad \Delta \Vdash a : r_\delta \langle B \rangle}{\Delta \triangleright a! \langle v \rangle \xrightarrow{a!v}_\sigma \Delta \triangleright \mathbf{0}} \quad \delta \preceq \sigma$
$\frac{\text{(C-IN)} \quad \Delta \Vdash a : w_\delta \langle B \rangle \quad \Delta, \tilde{c} : \tilde{C} \Vdash v : B}{\Delta \triangleright a?(X : A) P \xrightarrow{(\tilde{c} : \tilde{C})a?v}_\sigma \Delta, \tilde{c} : \tilde{C} \triangleright P\{v/X\}} \quad \delta \preceq \sigma \quad \tilde{c} \notin \text{fn}(P)}$	
$\frac{\text{(C-OPEN)} \quad \Delta \triangleright P \xrightarrow{(\tilde{c} : \tilde{C})a!v}_\sigma \Delta' \triangleright P'}{\Delta \triangleright (\mathbf{n} \ w \ b : B) P \xrightarrow{(b : B)(\tilde{c} : \tilde{C})a!v}_\sigma \Delta', b : B \triangleright P'} \quad \begin{array}{l} b \neq a \\ b \in \text{fn}(v) \end{array}$	
$\frac{\text{(C-CTXT)} \quad \Delta \triangleright P \xrightarrow{\mu}_\sigma \Delta' \triangleright P'}{\Delta \triangleright *P \xrightarrow{\mu}_\sigma \Delta' \triangleright *P \mid P'}$ $\Delta \triangleright \rho[P] \xrightarrow{\mu}_\sigma \Delta' \triangleright \rho[P']$ $\frac{\Delta \triangleright P \xrightarrow{\mu}_\sigma \Delta' \triangleright P'}{\Delta \triangleright P \mid \Delta' \triangleright P' \xrightarrow{\mu}_\sigma \Delta' \triangleright P' \mid \Delta \triangleright P} \quad \text{bn}(\mu) \notin \text{fn}(Q)$ $\frac{\Delta \triangleright P \xrightarrow{\mu}_\sigma \Delta' \triangleright P'}{\Delta \triangleright (\mathbf{n} \ w \ a : A) P \xrightarrow{\mu}_\sigma \Delta' \triangleright (\mathbf{n} \ w \ a : A) P'} \quad a \notin \text{n}(\mu)$	

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Let  $T$  be a test such that  $\Gamma \Vdash^\sigma T$ . Then  $P$  can interact with  $T$  by performing the action  $\mu$  and evolving to  $P'$ . As a result of this interaction, the capabilities of the context may be increased, as reflected in  $\Gamma'$ .

The modified LTS is defined in Figure 5 and the rules are straightforward. However note that in the rule (C-OUT) it is understood that the environment already knows the value  $v$  being output; it is only in the rule (C-OPEN) where the environment learns new information.

Some properties of this modified LTS are easy to establish. For example in  $\Gamma \triangleright P \xrightarrow{\mu}_\sigma \Gamma' \triangleright P'$  the new environment  $\Gamma'$  is completely determined by  $\Gamma$  and the action  $\mu$ . If  $\mu$  is  $\tau$  then  $\Gamma'$  coincides with  $\Gamma$ ; otherwise it is  $\Gamma$  augmented with the type environment  $\mathcal{E}(\mu)$ , the bound names together with their declared types. For this reason the following Lemma is easily established:

LEMMA 5.4.  $\Gamma \triangleright P \xrightarrow{\mu}_\sigma \Gamma' \triangleright P'$  and  $\Gamma \Vdash P$  implies  $\Gamma' \Vdash P'$ .

*Information Flow vs. Resource*

Note that in this Lemma the requirement  $\Gamma \Vdash P$  is essential to ensure that if  $T$  receives a value  $v$  then that value is compatible with the type environment  $\Gamma$ .

May testing is determined by the *traces*,  $s, t$ , in  $VAct^*$  which processes can perform. Let  $\epsilon$  represent the empty trace. The notion of complementary actions lifts element-wise to traces,  $\bar{s}$ . The names in a trace  $n(s)$  is defined as the union of the names in the individual actions; likewise the bound names in a trace  $bn(s)$  is defined as the union of the bound names in the individual actions.

DEFINITION 5.7 (TRACES). Let  $\Gamma \triangleright P \xrightarrow{s}_\sigma \Gamma' \triangleright P'$  be the least relation such that:

(TR- $\epsilon$ )

$$\frac{}{\Gamma \triangleright P \xrightarrow{\epsilon}_\sigma}$$

DEFINITION 5.9. (Asynchronous traces) Let  $\Gamma \triangleright P \xrightarrow[\sigma]{s}^a \Gamma' \triangleright$  be the least relation which, in addition to the clauses in Definition 5.7, satisfies

$$\begin{array}{c}
 \text{(C-AIN)} \\
 \Gamma \Vdash a : w_\delta \langle B \rangle, \\
 \Gamma, \tilde{c} : \tilde{C} \Vdash v : B, \\
 \Gamma, \tilde{c} : \tilde{C} \triangleright P \mid \delta \llbracket a! \langle v \rangle \rrbracket \xrightarrow[\sigma]{s}^a \Gamma \triangleright \\
 \hline
 \Gamma \triangleright P \xrightarrow[\sigma]{(\tilde{c} : \tilde{C}) a? v. s}^a \Gamma' \triangleright
 \end{array}
 \quad
 \begin{array}{l}
 \delta \preceq \sigma \\
 \tilde{c} \notin \text{fn}(P)
 \end{array}$$

□

The ability to compose asynchronous traces depends on the fact that

- $\rho[[P]] \xrightarrow{(\tilde{c}:\tilde{C}a!v)} \rho[[P']]$  because  $P \xrightarrow{(\tilde{c}:\tilde{C}a!v)} P'$ .  
 $\Gamma \Vdash^\sigma \rho[[P]]$  implies  $\Gamma \Vdash^{\sigma \sqcap \rho} P$  and so by induction

$$P \equiv_\Gamma (\mathbf{n} \ \mathbf{w} \ \tilde{c}:\tilde{C}) (\delta[[a!\langle v \rangle]] \mid P')$$

for some  $\delta \preceq \sigma \sqcap \rho$ . Using the rules (S-SRNEW)(S-SRSR) and (S-SRPAR) we can then show  $\rho[[P]] \equiv_\Gamma (\mathbf{n} \ \mathbf{w} \ \tilde{c}:\tilde{C}) (\rho \sqcap \delta[[a!\langle v \rangle]] \mid \rho[[P']])$ . □

**PROPOSITION 5.12 (TRACE COMPOSITION).** *Suppose  $\Gamma \Vdash^\sigma T$ .*



*Proof.* The proof is by induction on the derivation of  $\Gamma \triangleright P \mid H \xRightarrow{s}_\sigma^a$ . We examine the most interesting cases.

- $\Gamma \triangleright P \mid H \xrightarrow{\tau}_\sigma \Gamma \triangleright R \xRightarrow{s}_\sigma^a$ .

The most important case here is when there is communication between  $P$  and  $H$ . Here  $P \xrightarrow{\alpha} P'$ ,  $H \xrightarrow{\bar{\alpha}} H'$ ,  $R$  is  $(n \ w \ \tilde{c} : \tilde{C}) (P' \mid H')$ , where  $\tilde{c}$  are the bound variables in  $\alpha$ . There are two possibilities.

- Output from  $P$  to  $H$ ;  $\alpha$  has the form  $(\tilde{c} : \tilde{C})a!v$ . Let us examine the trace  $\Gamma \triangleright (n \ w \ \tilde{c} : \tilde{C}) (P' \mid H') \xRightarrow{s}_\sigma^a$ . Somewhere in  $s$  the names in  $\tilde{c}$  may be exported. In general we can construct a related trace  $s_c$  such that  $\Gamma, \tilde{c} : \tilde{C} \triangleright (P' \mid H') \xRightarrow{s_c}_\sigma^a$ , with the property that for any  $s$ ,  $\Gamma, \tilde{c} : \tilde{C} \triangleright \xRightarrow{s}_\sigma^a$  implies  $\Gamma \triangleright \xRightarrow{s_c}_\sigma^a$ ;  $s_c$  is obtained from  $s$  by omitting any bounds  $(c : C)$  found on its output actions.

Now we may apply induction to  $\Gamma, \tilde{c} : \tilde{C} (P' \mid H') \triangleright \xRightarrow{s_c}_\sigma^a$ , since  $\Gamma \Vdash^\sigma P'$  by Subject Reduction and  $\Gamma \triangleright \xRightarrow{s_c}_\sigma^a$  to  $\Gamma \triangleright \xRightarrow{s}_\sigma^a$ .



$\Gamma, \tilde{c} : \tilde{C} \triangleright (P \mid \delta[a!\langle v \rangle])) \xrightarrow{s'}^a_\sigma$ . Again we may now use Definition 5.9 to obtain the required  $\Gamma, \tilde{c} : \tilde{C} \triangleright P \xrightarrow{s}^a_\sigma$ .

□

Given this technical result, we can now prove the Non-Interference Theorem.

**THEOREM (5.3).** *If  $\Gamma \Vdash^\sigma P$ , and  $\Gamma \Vdash^{\text{top}} H, K$  where  $H, K$  are  $\sigma$ -free processes, then:*

$$P \simeq_\Gamma^\sigma \quad \text{implies} \quad P \mid H \simeq_\Gamma^\sigma \mid K.$$

*Proof.* To establish the result, it is sufficient to show that  $P \simeq_\Gamma^\sigma P \mid H$ . In fact by Theorem 5.14 it is sufficient to show  $\Gamma \triangleright P \xrightarrow{s}^a_\sigma$  implies  $\Gamma \triangleright P \mid H \xrightarrow{s}^a_\sigma$ , which is immediate, and  $\Gamma \triangleright P \mid H \xrightarrow{s}^a_\sigma$  implies  $\Gamma \triangleright P \xrightarrow{s}^a_\sigma$ ; this follows from the previous Proposition. □

Note that the requirement that  $P, H, K$  be well-typed processes at level  $\sigma$  is necessary for this result to be true. For example consider the process  $P$  defined by  $h?(x) l?y. \mathbf{0}$  in an environment  $\Gamma$  in which  $h, l$  are high-level and low-level resources respectively. Then  $P \simeq_\Gamma^{\text{bot}} \mathbf{0}$ . However  $P \mid H \not\simeq_\Gamma^{\text{bot}} H$ , where  $H$  is the high-level process  $h!\langle \rangle$ .

## 6 Conclusions and Related Work

In this paper we have proposed simple typing systems for enforcing a variety of security properties for the *security  $\pi$ -calculus*. The types are obtained by adding security levels to the standard input/output types of the  $\pi$ -calculus, [21, 23]. The first typing system, based on *R-Types*, is designed with resource access control in mind; the security level of a resource (or more formally a capability on a resource) dictates the security clearance of the process using it.

a high-level process reads a value from a low-level channel

licated type system is used to control information flow. The judgements in their system take

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